On Packet Scheduling with Adversarial Jamming and Speedup

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Goal of this talk

Simple online scheduling model with an open problem.

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Outline

- Model
- Algorithm
- Local analysis
- Non-local analysis
- Lower bounds

[Anta, Georgiou, Kowalski, Widmer, Zavou '13], [Jurdzinski, Kowalski, Loryś '14]

- Packets of sizes $\ell_1 < \cdots < \ell_k$, released over time, no deadlines
- Single channel (machine), no preemption
- Objective: Total size of completed packets

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Adversarial errors, immediately known, retransmission possible



Goal

- *R*-competitive algorithms, i.e., $OPT \le R \cdot ALG + C$
- k and ℓ_k are constants, allowed in C

M. Böhm, Ł. Jeż, J. Sgall, P. Veselý Packet Scheduling w. Adversarial Jamming and Speedup

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We focus on deterministic algorithms only.

M. Böhm, Ł. Jeż, J. Sgall, P. Veselý Packet Scheduling w. Adversarial Jamming and Speedup

General instances

• 3-competitive algorithm

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Speedup for 1-competitiveness on general instances

- a lower bound of 2 [Anta et al. '15]
- but no good algorithm

Speedup s = ALG needs time only ℓ/s to send a packet of size ℓ

Simple generic local analysis

• 3-competitive

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- 1-competitive with speedup 6

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Upper bound

• 1-competitive with speedup 4 (tight)

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Upper bound

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Lower bounds

No 1-competitive deterministic algorithm with speedup $s < \phi + 1 pprox 2.618$

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Algorithm – Properties

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Nice properties of the new algorithm

- no unnecessary idle time
- the same algorithm for all speeds
- no need to know the speed and packet sizes in advance

Algorithm – Observations

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Observation: In each phase

If the 1st packet completes:
 size of completed packets ≥ 1/2 of length of the phase.

 ℓ_i

ALG:

• All jobs are completed if no fault or arrival occurs.

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ALG:

 $\ell_i < 2\ell_i$

 $< \ell_{i-1}$

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Proof method (for 1-competitiveness)

For each phase within $(C_i, C_{i-1}]$, show that the total size of long packets $(\geq \ell_i)$ completed by ALG is at least that of ADV

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 - Does not hold for speedup below 4

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- Y completed as $Y < 2\ell_i$
 - Does not hold for speedup below 4
- It may happen that $X = \ell_{i+1} > \ell_i = Y \dots$
 - ... but only if no packet of size ℓ_{i+1} is pending.

Redefine critical times: C'_i satisfy:

• almost no packets of size ℓ_i pending just before C'_i ,

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Focus on packets of size ℓ_i



+ quite a lot of technical work (e.g., phases in which ALG completes no packet)

No deterministic 1-competitive algorithm with speedup <2

Input

- packet sizes 1 and ℓ , all packets arrive at time 0
- # of small packets \gg # of ℓ -packets

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Adversary strategy in each phase

• If ALG starts ℓ soon, interrupt it and finish more small packets than ALG.

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+ when ADV completes all packets ϕ^i , then it completes packets < ϕ^i preventing ALG to finish a packet $\geq \phi^i$

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- Tradeoffs (e.g., speed vs. competitive ratio)



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Problem Algorithm Lower bounds

Local analysis results for special cases

Divisible instances (ℓ_i divides ℓ_{i+1} for each *i*):

• Our algorithm is 1-competitive with speedup 2.5

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Local analysis results for special cases

Divisible instances (ℓ_i divides ℓ_{i+1} for each *i*):

• Our algorithm is 1-competitive with speedup 2.5 Well-separated instances:

- $\ell_{i+1} \ge \alpha \ell_i$ for some parameter $\alpha > 1$
- Our algorithm is 1-competitive with speedup S_{α} :



Algorithm for divisible instances

Start phase Run packet of the largest size ℓ_j such that $P^{<j} < \ell_j$, $P^{<j}$: total size of pending packets smaller than ℓ_j . Regular step Run ... largest ℓ_j such that $\ell_j \leq S$ and ℓ_j divides S, S: total size of packets completed in this phase.

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- Same properties as the algorithm for general instances
- Key observation: if a packet of size l_i is pending during the whole phase, then the size of completed smaller packets is at most 1 · l_i.

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Results using local analysis

- 2-competitive (optimal),
- 1-competitive with speedup 2 (optimal),
- both algorithmic results also done by [Jurdzinski et al.]